

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	@ad<="20021029" cross near2 multiplexer\$2 port\$2 delay near buffer synchroniz\$4	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:29
S2	22	@ad<="20021029" cross near2 multiplexer\$2 port\$2 delay near5 buffer synchroniz\$4	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:22
S3	22	@ad<="20021029" cross near2 multiplexer\$2 port\$2 delay near3 buffer synchroniz\$4	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:24
S4	22	@ad<="20021029" cross near2 multiplexer\$2 delay near3 buffer synchroniz\$4	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:24
S5	22	@ad<="20021029" cross near2 multiplexer\$2 delay near3 buffer synchroniz\$4 interconnect\$2	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:50
S6	0	@ad<="20021029" cross near2 multiplexer\$2 delay near buffer synchroniz\$4 interconnect\$2	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:26
S7	16	@ad<="20021029" cross near2 multiplexer\$2 delay near2 buffer synchroniz\$4 interconnect\$2	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:28
S8	2	@ad<="20021029" cross near2 multiplexer\$2 delay adj2 buffer synchroniz\$4 interconnect\$2	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:29
S9	78	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 interconnect\$2	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:29

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S10	57	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:31
S11	0	@ad<="20021029" cross near5 multiplexer\$2 delay adj4 buffersynchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:31
S12	3	@ad<="20021029" cross near5 multiplexer\$2 delay adj4 buffer synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:32
S13	34	@ad<="20021029" cross with multiplexer\$2 delay adj4 buffer synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:32
S14	33	@ad<="20021029" cross with multiplexer\$2 delay adj2 buffer synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:32
S15	2	@ad<="20021029" cross near2 multiplexer\$2 delay adj2 buffer synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:33
S16	0	@ad<="20021029" cross near2 multiplexer\$2 near bus synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:33
S17	57	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:39
S18	110	@ad<="20021029" link near2 multiplexer\$2 synchroniz\$4 interconnect\$2 length	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:39
S19	31	@ad<="20021029" link near2 multiplexer\$2 synchroniz\$4 interconnect\$2 length delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:40

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S20	31	@ad<="20021029" link near2 multiplexer\$2 synchroniz\$4 interconnect\$2 delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:42
S21	39	@ad<="20021029" link near2 multiplexer\$2 synchroniz\$4 delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:40
S22	0	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:41
S23	39	@ad<="20021029" link near2 multiplexer\$2 synchroniz\$4 delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:41
S24	40	@ad<="20021029" link near2 multiplexer\$2 delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:41
S25	0	@ad<="20021029" cross near2 multiplexer\$2 delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:42
S26	123	@ad<="20021029" cross near2 multiplexer\$2 buffer delay	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:42
S27	32	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 interconnect\$2 delay with buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:42
S28	32	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 interconnect\$2 delay same buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:43
S29	57	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 interconnect\$2 delay buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:44

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S30	71	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 delay buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:44
S31	81	@ad<="20021029" cross near2 multiplexer\$2 synchroniz\$4 buffer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:45
S32	0	@ad<="20021029" amirichimeh-abbas.in.	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:47
S33	0	@ad<="20021029" amirichimeh-a.in.	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:45
S34	0	@ad<="20021029" amirichimeh-a\$.in.	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:45
S35	0	@ad<="20021029" baumer-howard.in.	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:46
S36	16	baumer-howard.in.	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:46
S37	0	@ad<="20021029" (cross adj link) near multiplexer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:48
S38	0	@ad<="20021029" (cross near link) near multiplexer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:48
S39	289	@ad<="20021029" cross near2 multiplexer	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:49
S40	25	@ad<="20021029" cross near2 multiplexer length with interconnect	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:50
S41	10	@ad<="20021029" cross near2 multiplexer length near3 interconnect	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:50
S42	8	@ad<="20021029" cross near2 multiplexer length near3 equal	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:53
S43	146	@ad<="20021029" cross near2 multiplexer interconnect\$2	US-PGPU B; USPAT	AND	OFF	2007/04/25 15:53
S44	441	@ad<="20021029" cross near2 multiplexer\$2	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:30

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S45	34	@ad<="20021029" (cross near link) with multiplexer\$2	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:33
S46	16	@ad<="20021029" (cross near link) with multiplexers	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:34
S47	16	@ad<="20021029" (cross adj link) with multiplexers	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:34
S48	207	@ad<="20021029" cross near2 multiplexers	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:34
S49	0	@ad<="20021029" (cross near2 multiplexers) same circular	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:34
S50	0	@ad<="20021029" (cross near2 multiplexers) same spherical	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:35
S51	26	@ad<="20021029" (cross near2 multiplexers) same configuration	US-PGPU B; USPAT	AND	OFF	2007/05/07 15:35
S52	27	@ad<="20021029" cross near2 multiplexer\$2 delay near3 buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:50
S53	1	@ad<="20021029" cross near2 multiplexer\$2 delay near buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:51
S54	21	@ad<="20021029" cross near2 multiplexer\$2 delay near2 buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:51
S55	16	@ad<="20021029" cross near2 multiplexers delay near2 buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:51
S56	16	@ad<="20021029" cross near multiplexers delay near2 buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:52
S57	630	@ad<="20021029" multiplexers delay near2 buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:52
S58	240	@ad<="20021029" multiplexers delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:52

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S59	0	@ad<="20021029" cross same multiplexers delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:52
S60	0	@ad<="20021029" interconnects same multiplexers delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:52
S61	19	@ad<="20021029" pair same multiplexers delay adj buffer	US-PGPU B; USPAT	AND	OFF	2007/05/10 07:52